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Paper No./Mail Date

of Biological Material

4.

Examiner's Comment Regarding Requirement for Deposit

9. Other _____.

8. X Examiner's Statement of Reasons for Allowance

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Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance:

the prior art, individually or in combination does not teach an integrated circuit multiprocessor apparatus for processing a plurality of data packets comprising a memory comprising a plurality of descriptors, each of the plurality of descriptors comprising an ownership indication, a start of packet indication, an end of packet indication, a buffer length value and a buffer address for specifying a location of a buffer in memory for storing at least a portion of a data packet; and a packet manager circuit being configured to write back to memory all descriptors associated with a data packet upon completion of the transfer of the data packet and the descriptors are written back in the order in which they are released;

the prior art, individually or in combination does not teach a system for controlling distribution of a plurality of data packets to a plurality of processors, the system comprising a DMA controller releasing ownership of a plurality of consecutive descriptor entries in order upon completing storage of a data packet; and an assignment processor for assigning a first group of descriptors from the plurality of consecutive descriptor entries to a first processor and for assigning a second group of descriptors from the plurality of consecutive descriptor entries to a second processor; where each processor in the plurality of processors is configured to spin on the final descriptor in its assigned group of descriptors and to process any packet comprising any EOP descriptors contained with its assigned group of descriptors along with any other descriptors associated with each EOP descriptor;

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the prior art, individually or in combination does not teach a method comprising assigning a first subset of a plurality of descriptors to a first processor and a second subset of the plurality of descriptors to a second processor; releasing the first descriptor group to software by writing the first descriptor group to memory; releasing the second descriptor group to software by writing the second descriptor group to memory; and at each processor, identifying any EOP descriptor in the assigned subset of the plurality of descriptors and processing any descriptor associated with said EOP descriptor.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tanh Quang Nguyen whose telephone number is (571) 272-4154 and whose e-mail address is tanh.nguyen36@uspto.gov. The examiner can normally be reached on Monday-Friday from 8:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dov Popovici, can be reached on (571) 272-4083. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306 for After Final, Official, and Customer Services, or (571) 273-4154 for Draft to the Examiner (please label "PROPOSED" or "DRAFT").

Effective May 1, 2003 are new mailing address is:
Mail Stop
Commissioner for Patents

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P.O. Box 1450

Alexandria, VA 22313-1450

Effective December 1, 2003, hand-carried patent application related incoming correspondences will be to a centralized location.

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PRIMARY EXAMINER

TQN July 10, 2005